**INTERNATIONAL INSTITUTE OF INFORMATION TECHNOLOGY**

**SPRING 2018-19**

**E2.103 ANALOG ELECTRONIC CIRCUITS**

**ASSIGNMENT 6**

CA3096 chip has three npn and two pnp transistors The data sheet can be obtained from internet. Your mission is to design an op amp excluding the power amplifier stage to get an open loop gain of at least 105

and a dominant pole at 10 Hz.You may use any number of CA3096. The differential input impedance should be.at least 10K and output impedance less than 5 ohms. The input terminals and output terminal must be at ground potential in quiescent state. Design biasing circuit also using current mirrors

**Minimum number of resistors should be used that also in the range of 10 ohms to 300 ohms**